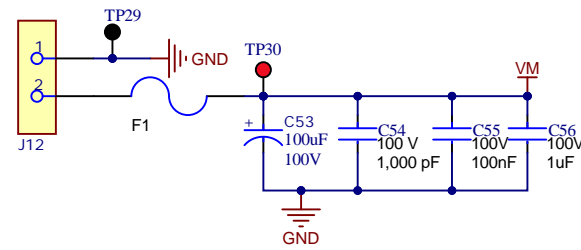


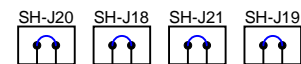
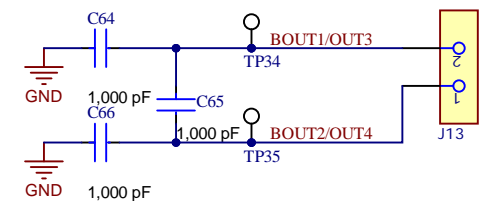
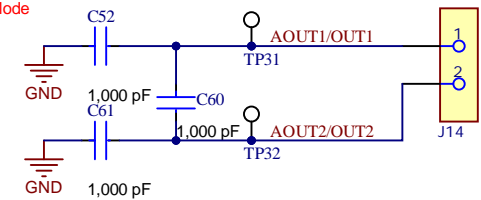
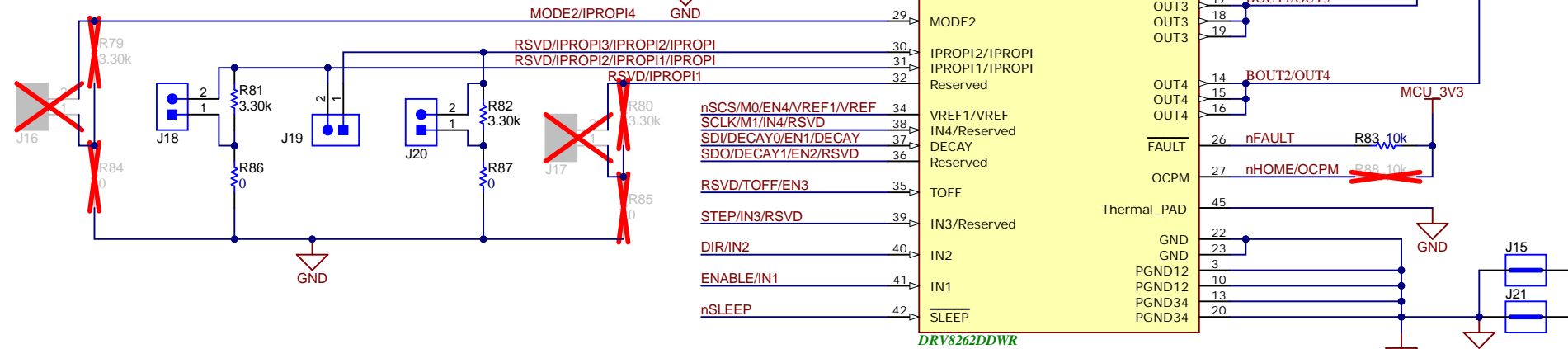
## Motor Driver

4.5 - 48V DC (DRV8452; DRV8252, DRV8952)

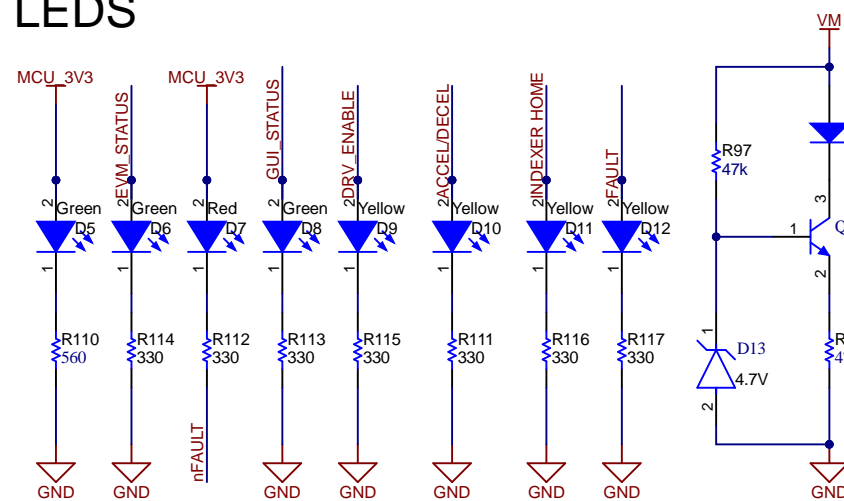
#### 4.5 - 60V DC (DRV8462; DRV8262, DRV8962)



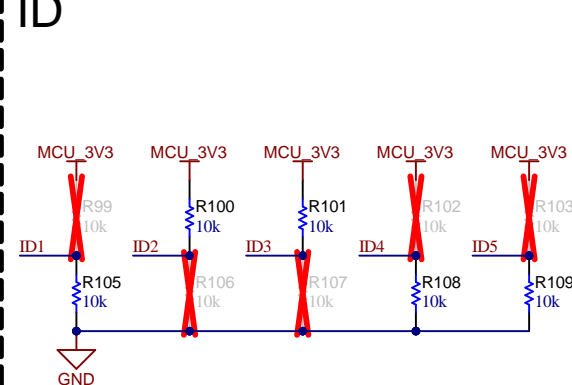
For DRV8962 and DRV8262 populate jumpers to disable current regulation



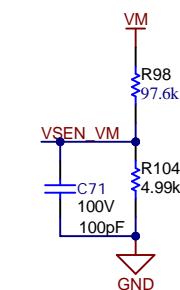
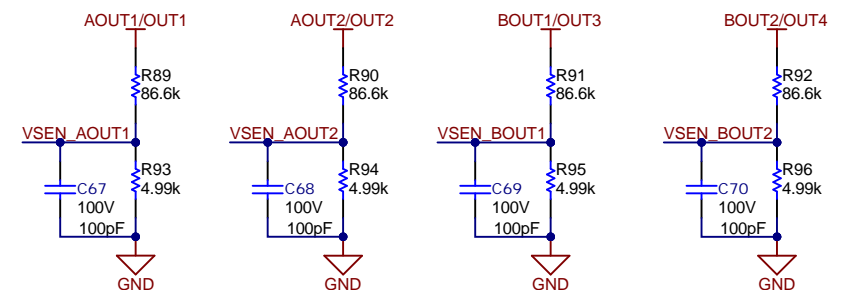
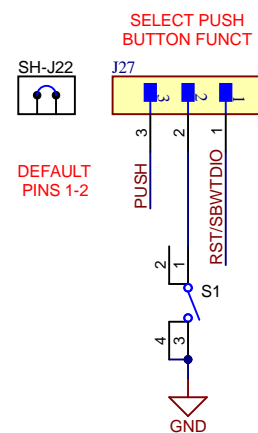
## LEDS




## ID

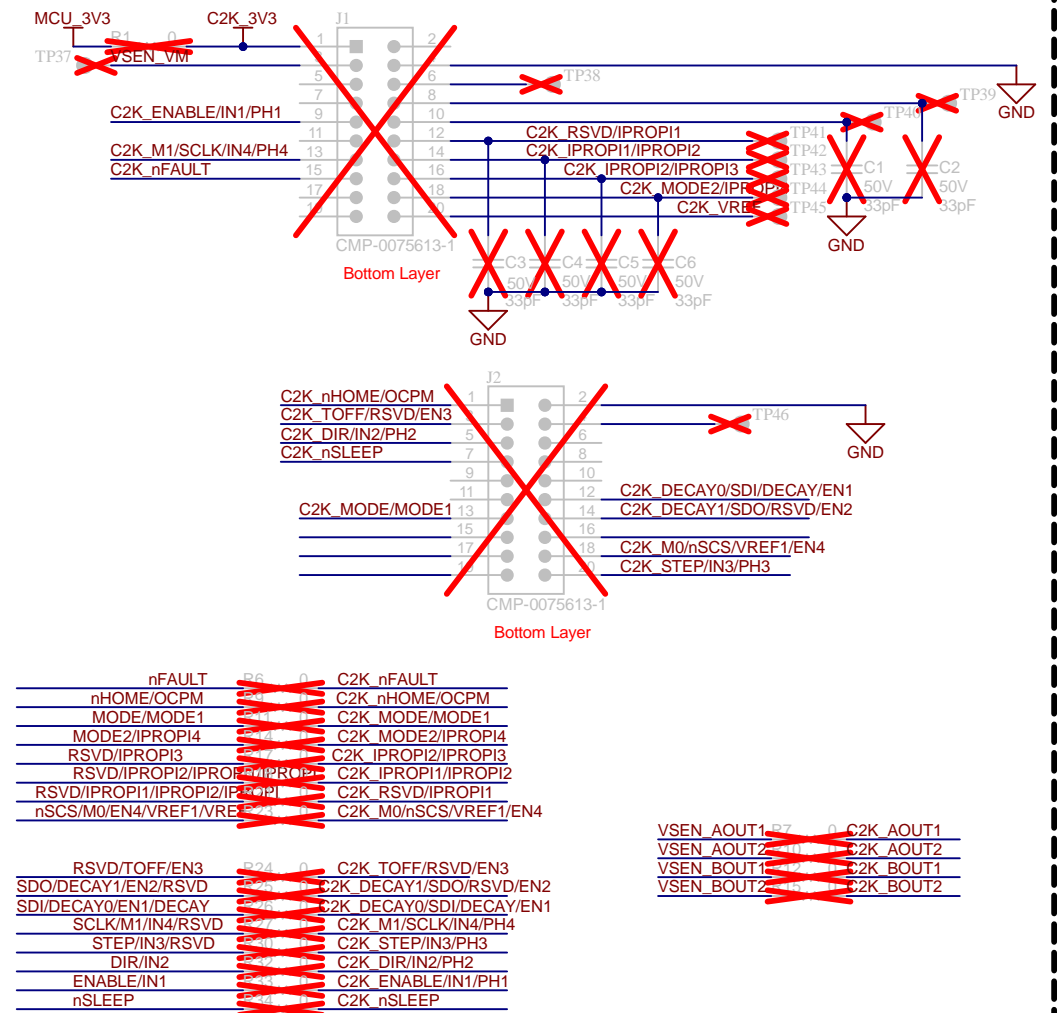
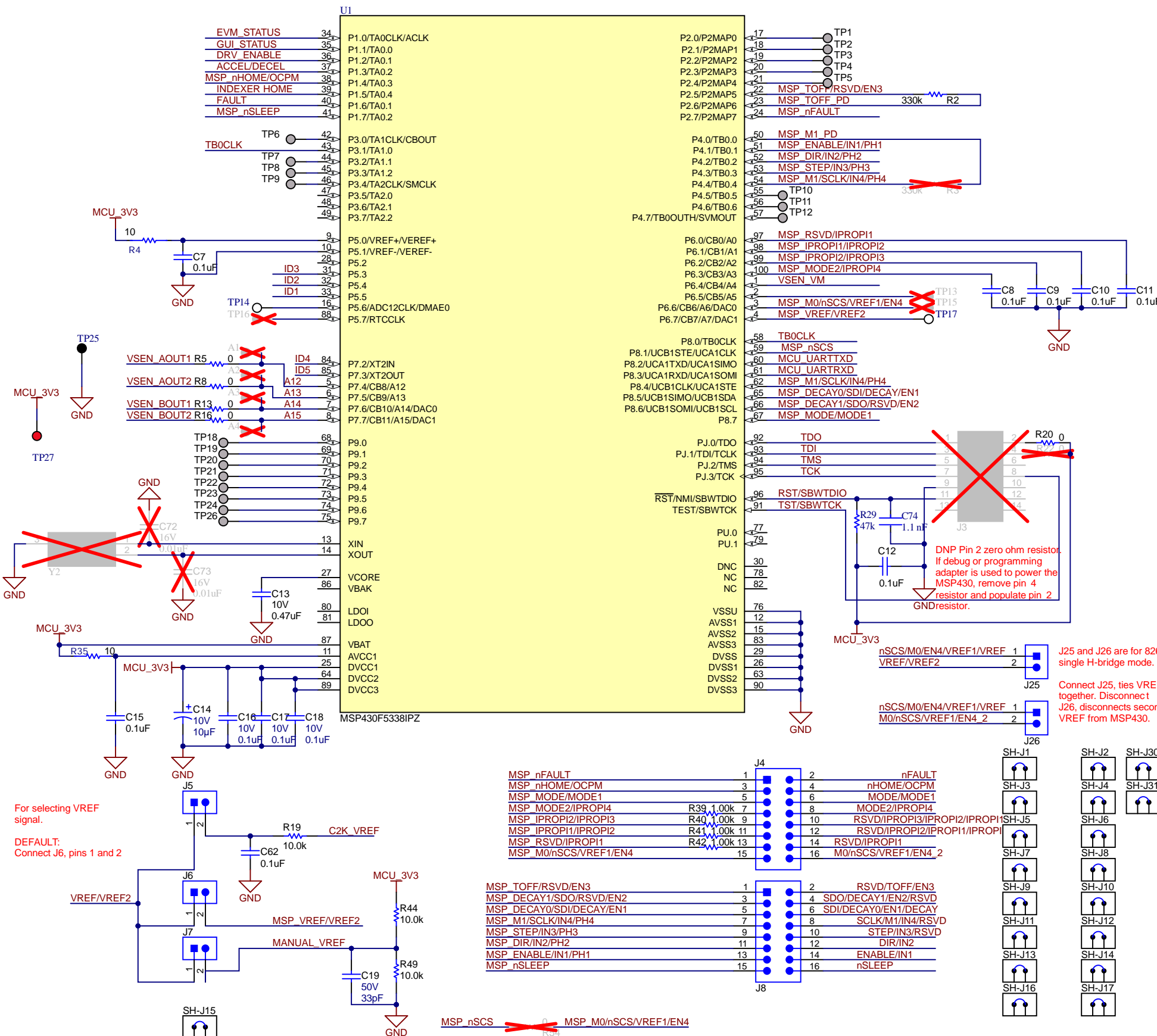
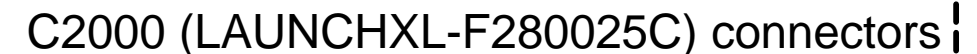


## BUTTON



Orderable:	Designed for: <a href="#">Public Release</a>	Mod. Date: 8/25/2022	 <b>TEXAS INSTRUMENTS</b>
TID #: <a href="#">N/A</a>	Project Title: <a href="#">DRV8452EVM</a>		
Number: <a href="#">MD040</a>	Rev: <a href="#">E2</a>	Sheet Title:	
SVN Rev: 1d5cd23cf125c4a53b0405	<a href="#">025ef300072a685d06</a> [Locally Modified]	Sheet: <a href="#">2</a> of <a href="#">3</a>	
Drawn By:	File: <a href="#">MD040_Motor_Driver_RevE2_SchDoc</a>	Size: B	
Engineer: <a href="#">Wang Li</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>	© Texas Instruments 2021

## MSP430F5338



nFAULT	R66	0	C2K nFAULT	
nHOME/OCPM	R67	0	C2K nHOME/OCPM	
MODE/MODE1	R68	0	C2K MODE/MODE1	
MODE2/IPROP14	R69	0	C2K MODE2/IPROP14	
RSVD/IPROP13	R70	0	C2K IPROP12/IPROP13	
RSVD/IPROP12/IPROP11/PROPI2	R71	0	C2K IPROP11/IPROP12	
RSVD/IPROP11/IPROP12/IPROP1	R72	0	C2K RSVD/IPROP1	
nSCS/M0/EN4/VREF1/VREF2	R73	0	C2K M0/nSCS/VREF1/EN4	
RSVD/TOFF/EN3	R24	0	C2K TOFF/RSVD/EN3	
SDO/DECAY1/EN2/RSVD	R25	0	C2K DECAY1/SDO/RSVD/EN2	
SDI/DECAY0/EN1/DECAY	R26	0	C2K DECAY0/SDI/DECAY/EN1	
SCLK/M1/IN4/RSVD	R27	0	C2K M1/SCLK/IN4/PH4	
STEP/IN3/RSVD	R28	0	C2K STEP/IN3/PH3	
DIR/IN2	R29	0	C2K DIR/IN2/PH2	
ENABLE/IN1	R30	0	C2K ENABLE/IN1/PH1	
nSLEEP	R31	0	C2K nSLEEP	
			VSEN_AOUT1	C2K_AOUT1
			VSEN_AOUT2	C2K_AOUT2
			VSEN_BOUT1	C2K_BOUT1
			VSEN_BOUT2	C2K_BOUT2

J25 and J26 are for 8262 single H-bridge mode.

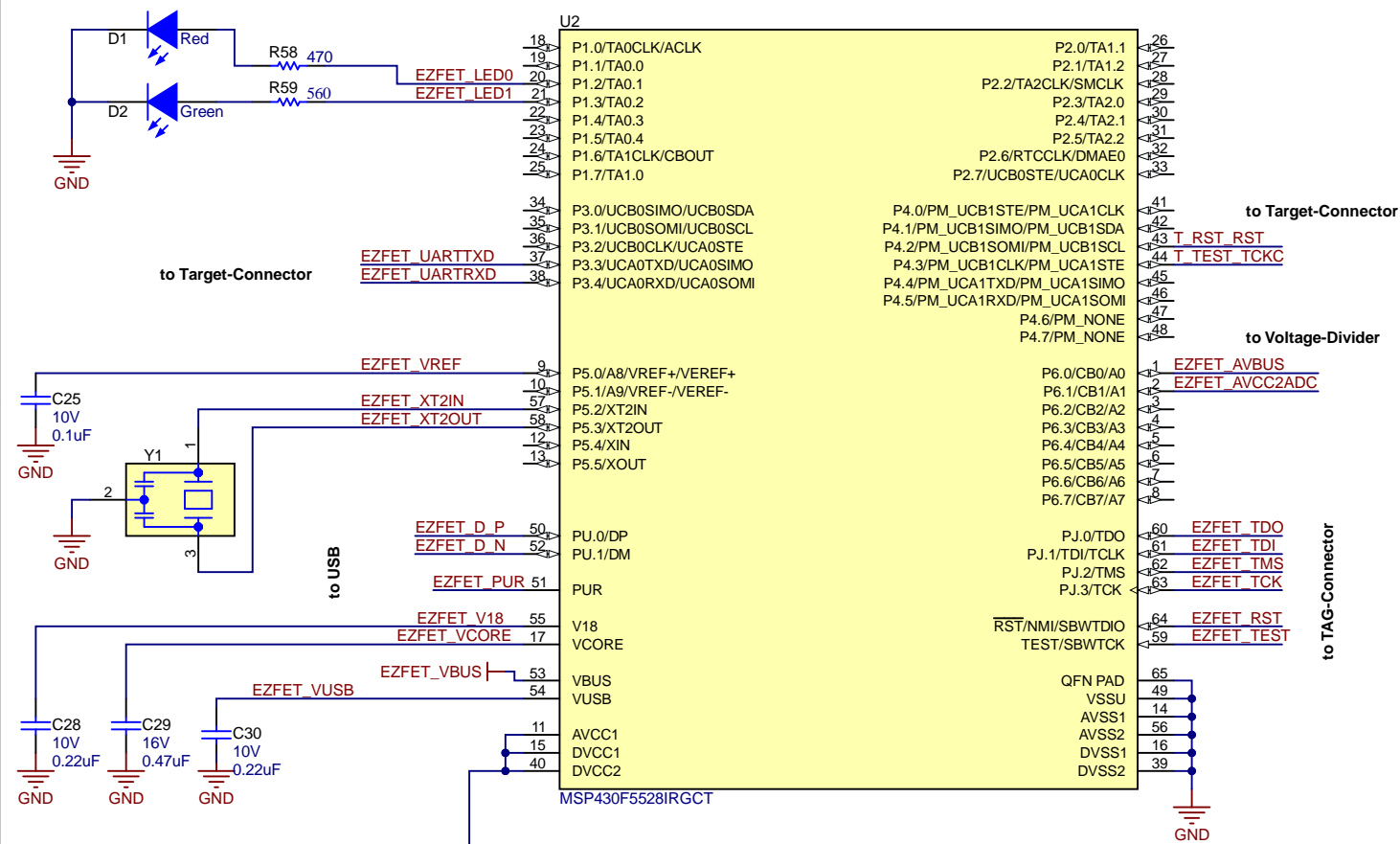
Connect J25, ties VREF s  
together. Disconnect t  
J26, disconnects second  
VREF from MSP430.

Left	Right
MSP nFAULT	2 nFAULT
MSP nHOME/OCPM	4 nHOME/OCPM
MSP MODE/MODE1	6 MODE/MODE1
MSP MODE2/IPROP14	8 MODE2/IPROP14
MSP IPROP2/IPROP3	10 RSDV/IPROP3/IPR
MSP IPROP1/IPROP2	12 RSDV/IPROP2/IPR
MSP RSDV/IPROP1	14 RSDV/IPROP1
MSP nSCS/VREF1/EN4	16 nSCS/VREF1/EN4_2

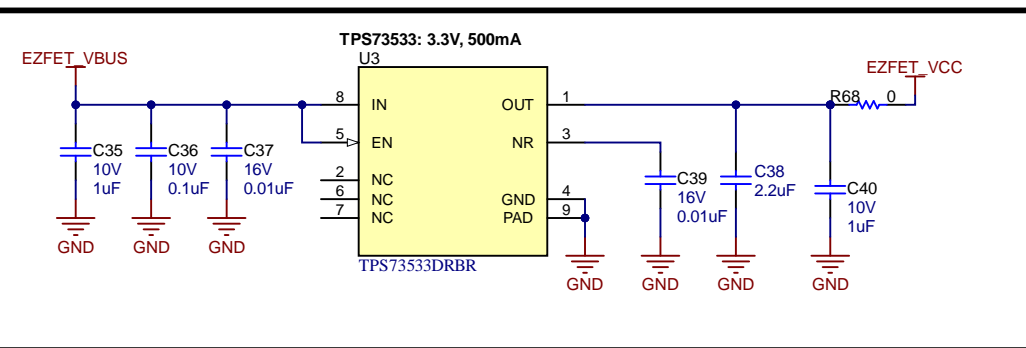
MSP_TOFF/RSVD/EN3	1	2	RSVD/TOFF/EN3
MSP_DECAY1/SDO/RSVD/EN2	3	4	SDO/DECAY1/EN2/RSVD
MSP_DECAY0/SDI/DECAY/EN1	5	6	SDI/DECAY0/EN1/DECAY
MSP_M1/SCLK/IN4/PH4	7	8	SCLK/M1/IN4/RSVD
MSP_STEP/IN3/PH3	9	10	STEP/IN3/RSVD
MSP_DIR/IN2/PH2	11	12	DIR/IN2
MSP_ENABLE/IN1/PH1	13	14	ENABLE/IN1
MSP_nSLEEP	15	16	nSLEEP

MSP\_nSCS MSP\_M0/nSCS/VREF1/EN4

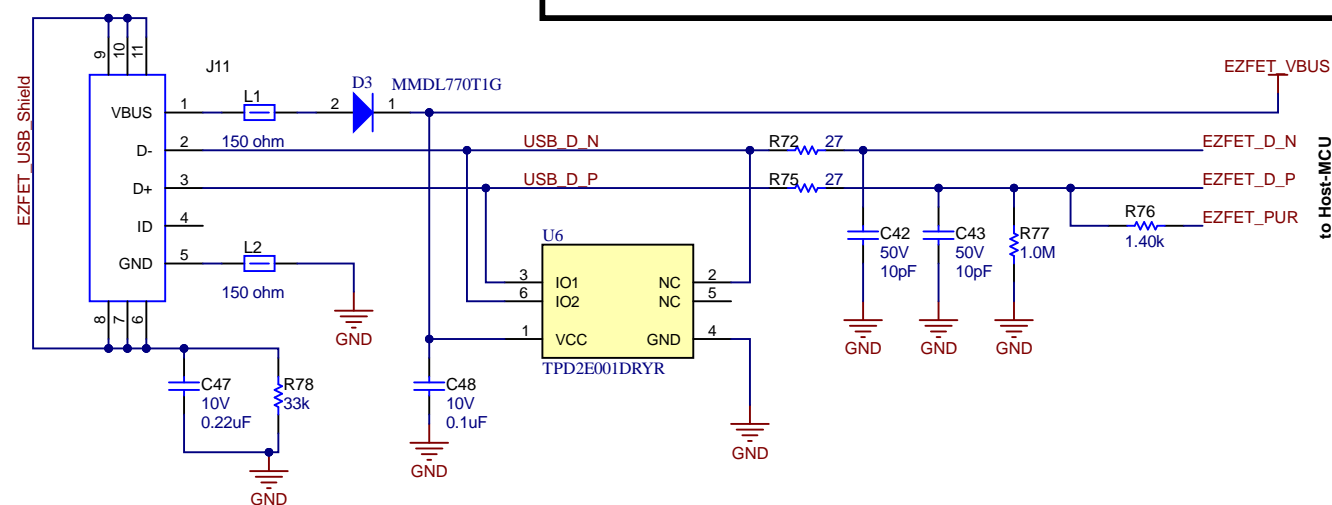
## Host MCU for Emulation



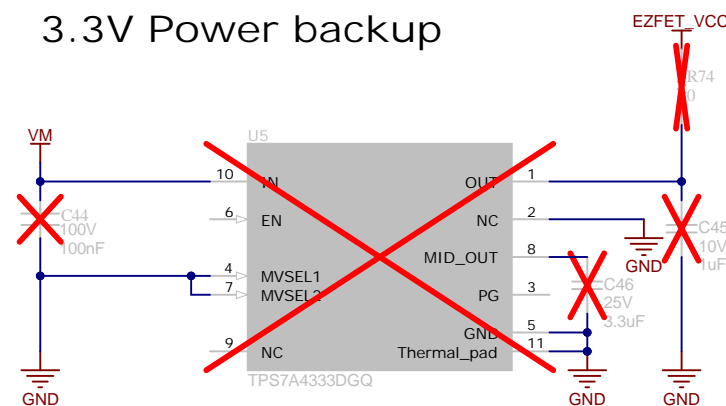
### 3.3V Power (EZFET\_VCC)



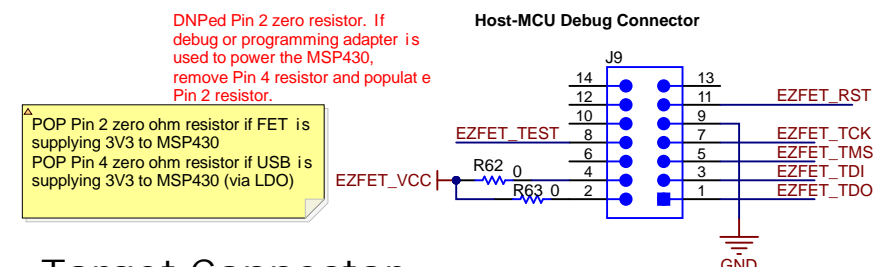
## USB-Interface



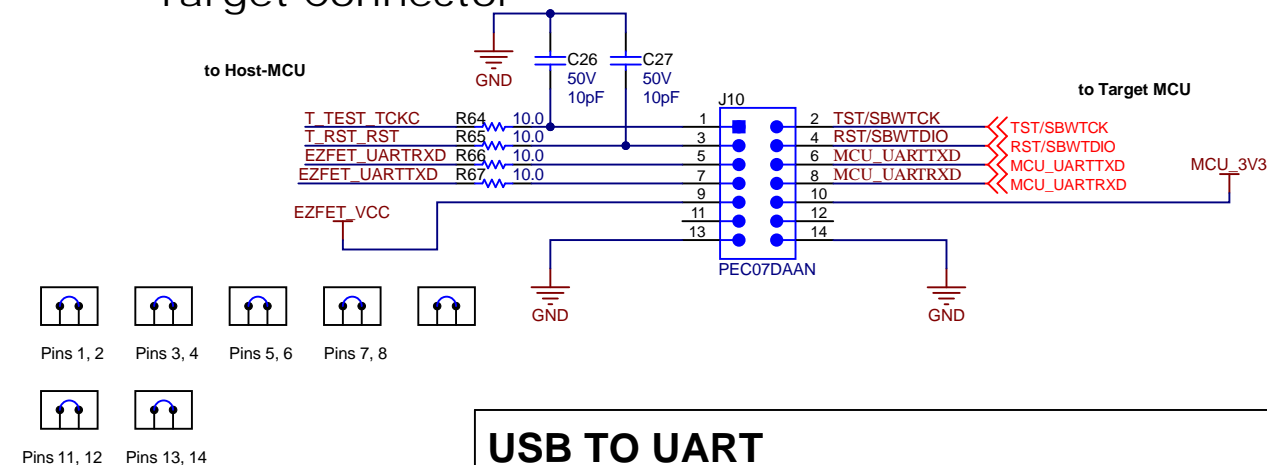
### 3.3V Power backup



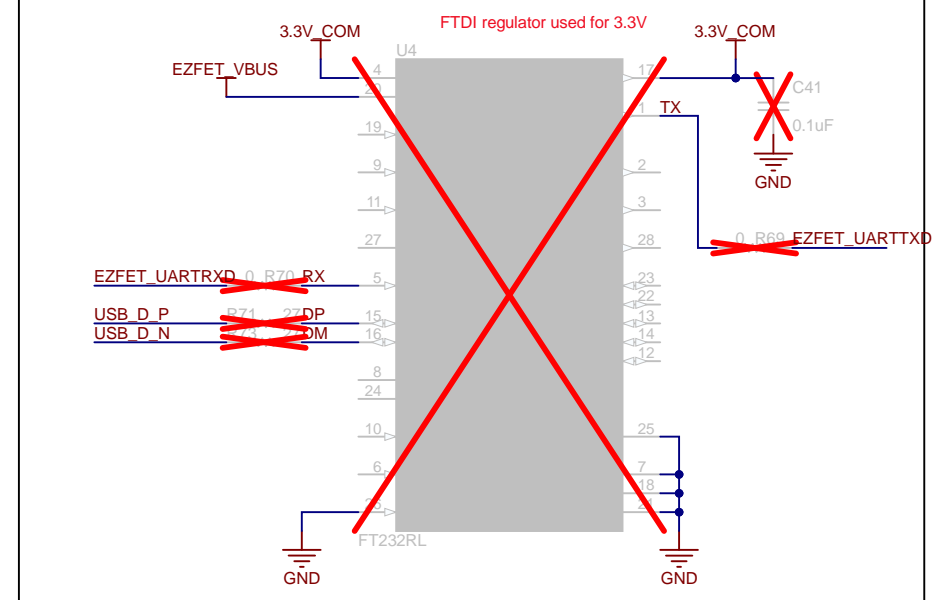
## JTAG-Connector (Host Debug)




## Target Connector

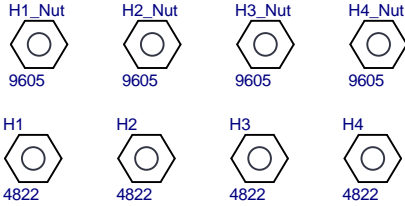


## USB TO UART



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Orderable:	Designed for: <b>Public Release</b>	Mod. Date: 8/5/2022	
TID #: <b>N/A</b>	Project Title: <b>DRV8452EVM</b>		
Number: <b>MD040</b>	Rev: <b>E2</b>	Sheet Title:	
SVN Rev: 192a167bf876ce2a492c1f1a6e602427a2b279106		Sheet: <b>2</b> of <b>3</b>	
Drawn By:	File: <b>MD040_ezFET_RevE2_SchDoc</b>	Size: <b>B</b>	
Engineer: <b>Wang Li</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>	© Texas Instruments 2021



PCB Number: MD040

PCB Rev: E2

PCB

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PCB

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FCC disclaimer

PCB

LOGO

WEEE logo



PCB

LOGO

CAUTION. READ USER GUIDE BEFORE USE

LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	DRV8452EVM
002	DRV8462EVM
003	DRV8952EVM
004	DRV8962EVM
005	DRV8252EVM
006	DRV8262EVM

Orderable:		Designed for: <a href="#">Public Release</a>	Mod. Date: 7/19/2022
TID #:	<a href="#">N/A</a>	Project Title: <a href="#">DRV8452EVM</a>	
Number:	<a href="#">MD040</a>	Rev: <a href="#">E2</a>	Sheet Title:
SVN Rev:	<a href="#">9388a18b6cde3fb7a9e5d343a4f72b1d9c0f5206</a>		Sheet: <a href="#">3</a> of <a href="#">3</a>
Drawn By:	<a href="#">Wang Li</a>		File: <a href="#">MD040_Hardware_RevE2.SchDoc</a>
Engineer:	<a href="#">Wang Li</a>		Contact: <a href="#">http://www.ti.com/support</a>